

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2015-16
III B.Tech II Semester ECE Section-A

Room No : 204 North Block (NB)

w.e.f : 21-12-2015

Day	930-1030	1030-1130	1130 - 1230	1230 - 1330	1330 - 1430	1430-1530	1530-1630	
Monday	DSP	MPMC	IPR	LUNCH	MEFA	IPR(G1-204) DC(G2-301) MEFA(G3-302)	VLSI	
Tuesday	MEFA	MEFA(G1-204) IPR(G2-301) DC (G3-302)	VLSI		<-----MPMC/DSP LAB----->			
Wednesday	VLSI	VLSI(G1-204) MPMC(G2-301) DSP (G3-302)	MEFA		MPMC	DC	DSP	
Thursday	<-----DSP/MPMC LAB----->				MPMC(G1-204) DSP(G2-301) VLSI (G3-302)	IPR	DC	
Friday	IPR	DC	DSP		DSP(G1-204) VLSI(G2-301) MPMC (G3-302)	VLSI	MPMC	
Saturday	DC	DSP	MPMC		DC(G1-204) MEFA(G2-301) IPR (G3-302)	MEFA	IPR	

S.No	Subject		Faculty	Dept
1	MEFA	Managerial Economics & Financial Analysis	E.Sowmya	MBA
2	IPR	Intellectual Property Rights	Saba Sulthana	ECE
3	DC	Digital Communications	A.Jyothirmai	ECE
4	VLSI	VLSI Design	K.Srinidhi Reddy	ECE
5	MPMC	Microprocessors & Microcontrollers	B.Kiranmayi	ECE
6	DSP	Digital Signal Processing	S.Surekha	ECE
7	MPMC LAB	Microprocessors & Microcontrollers Lab	B.Kiranmayi, K. Shiva Prasad	ECE
8	DSP LAB	Digital Signal Processing Lab	S.Surekha, A. Jyothirmai	ECE

Tutorial Groups :

G1:401-425

G2: 426-449

G3: 450-L412